



DWSLC0541NS ESD Protection Diode

Feature

- Bi-directional ESD protection of one line
- 35Watts peak pulse power ($t_p=8/20\mu s$)
- Working voltage: 5V
- Junction Capacitance: 3.5pF(Typ)
- Low clamping voltage
- Low leakage current
- IEC 61000-4-2 $\pm 20kV$ contact $\pm 20kV$ air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 3.5A (8/20 μs)

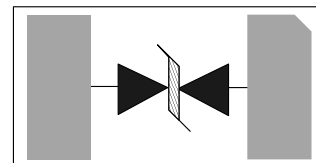
Application

- Cell Phone Handsets and Accessories
- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation

Mechanical Data

- Package: DFN0603-2L
- Molding compound flammability rating: UL 94V-0
- RoHS/WEEE Compliant

Schematic & PIN Configuration



Ordering Information

Part Number	Package	Marking	Packing	Reel Size
DWSLC0541NS	DFN0603-2L	F	10000 Tape & Reel	7 inches



Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power ($t_p=8/20\mu\text{s}$)	P_{PP}	35	Watts
Peak Pulse Current ($t_p=8/20\mu\text{s}$) (note1)	I_{pp}	3.5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	± 20 ± 20	kV
Lead Soldering Temperature	T_L	260 (10seconds)	$^{\circ}\text{C}$
Junction Temperature	T_J	-55 to + 125	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-55 to + 125	$^{\circ}\text{C}$

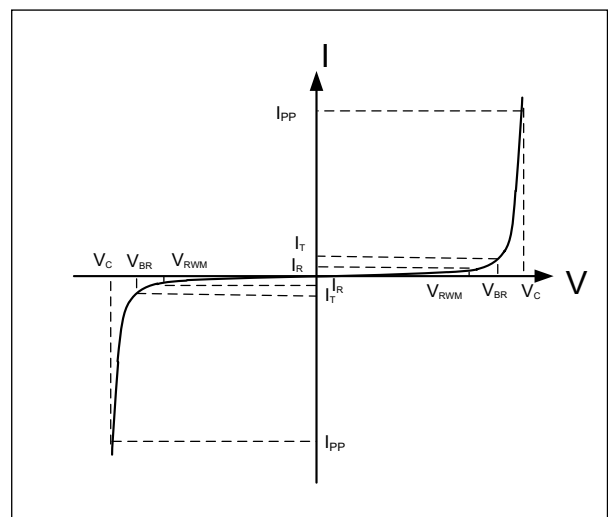
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typical	Max	Unit
Reverse Stand-Off Voltage	V_{RWM}				5.0	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1\text{mA}$	6.0	8.0		V
Reverse Leakage Current	I_R	$V_{RWM}=5\text{V}, T=25^{\circ}\text{C}$			0.5	μA
Peak Pulse Current	I_{PP}	$t_p=8/20\mu\text{s}$		3.5		A
Clamping Voltage	V_C	$I_{PP}=3.5\text{A}, t_p=8/20\mu\text{s}$		10		V
Junction Capacitance	C_J	$V_R = 0\text{V}, f = 1\text{MHz}$		3.5		pF

Electrical Parameters

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current

Note: 8/20 μs pulse waveform.





Typical Characteristic

Figure 1: Peak Pulse Power vs. Pulse Time

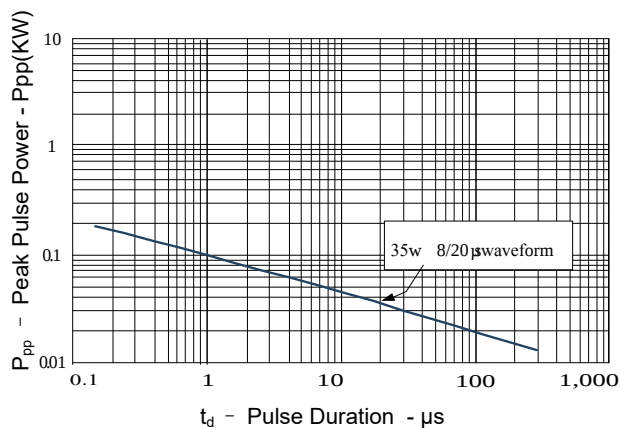


Figure 2: Power Derating Curve

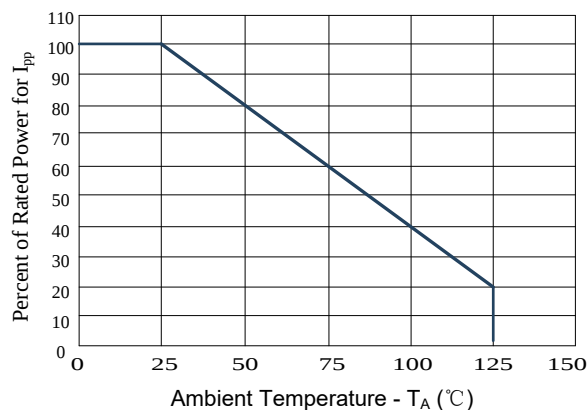


Figure3: Pulse Waveform

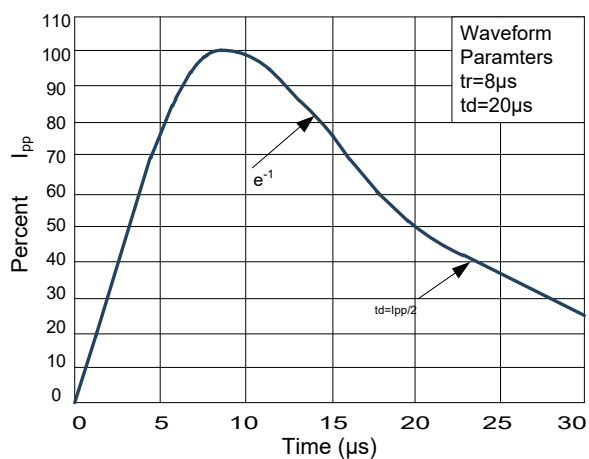


Figure 4: Clamping Voltage vs. Ipp

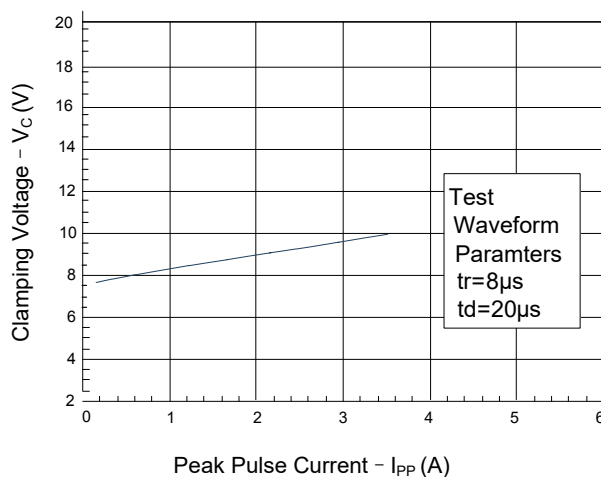


Figure5: Positive Clamping voltage (TLP)

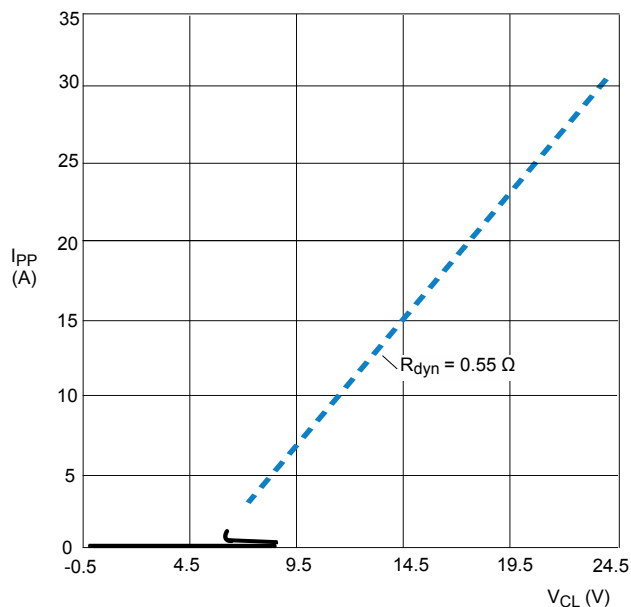
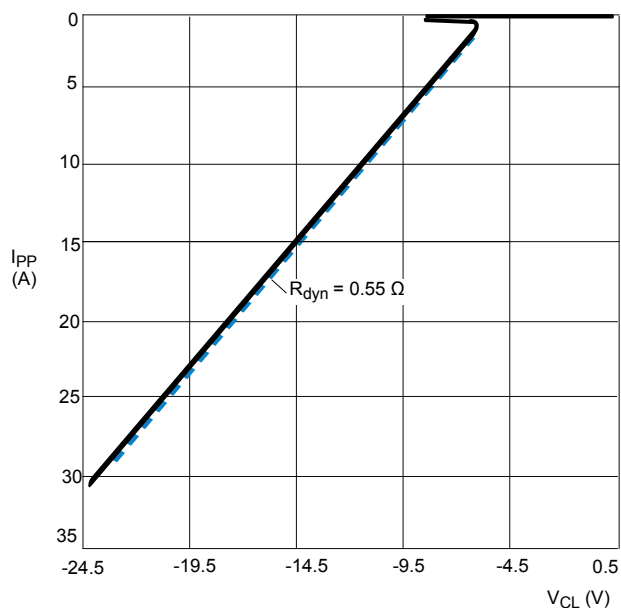
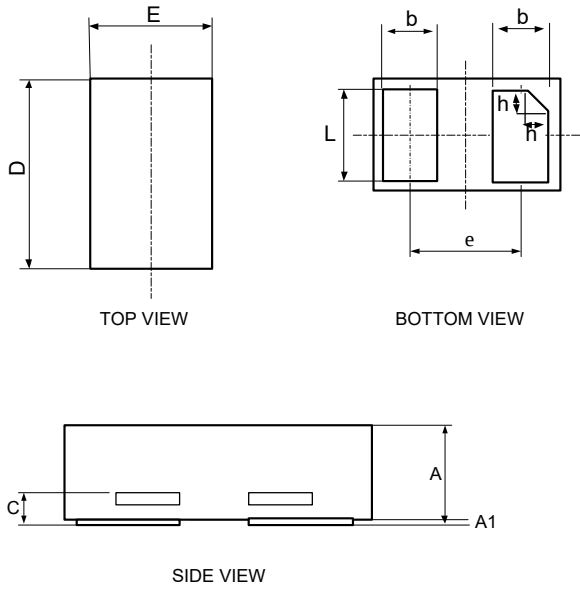


Figure5: Negative Clamping voltage (TLP)





DFN0603-2L Outline Drawing



Symbol	Dimensions in millimeters		
	Min	Nom	Max
A	0.28	0.30	0.34
A1	0.00	0.02	0.05
C	0.05	0.10	0.15
D	0.55	0.60	0.65
E	0.25	0.30	0.35
e	0.40		
b	0.13	0.19	0.24
L	0.20	0.25	0.30
h	0	0.05	0.10